IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Lizy Kurian John

SERIAL NUMBER:

08/512,505

FILING DATE:

August 8, 1995

FOR:

COMPUTER MEMORY CHIP WITH FIELD PROGRAMMABLE MEMORY CELL ARRAYS (FPMCAS), AND METHOD OF CONFIGURING

**EXAMINER:** 

T. Fears

GROUP ART UNIT:

2818

DATE OF NOTICE OF ALLOWANCE: July 29, 1998

ISSUE BATCH NUMBER:

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Assistant Commissioner for Patents

Washington, D.C. 20231

OCT 2 9 1998

LETTER TO THE OFFICIAL DRAFTSMAN

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Sir:

Enclosed herewith for filing are eight (8) sheets of formal drawings and two additional copies thereof illustrating Figs. 1-10 of the subject application in response to the Notice of Allowance referenced above.

Respectfully submitted,

Stefan V. Stein

Registration No. 29,702

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## CERTIFICATE OF EXPRESS MAILING

I HEREBY CERTIFY that the foregoing Letter to the Official Draftsperson along with eight (8) sheets of formal drawings and two additional copies thereof regarding the application of Lizy Kurian John, S/N 08/512,505, were placed in an envelope addressed to: The Commissioner for Patents, Washington, D.C. 20231, and mailed, postage paid, by U.S. Express Mail (EE571963567US), on this the 29th day of October, 1998.

The Commissioner is hereby authorized to charge any additional fees which may be required at any time during the prosecution of this application without specific authorization, or credit any overpayment, to Deposit Account Number 19-4127.

Stefan V Stein